

Notice of References Cited	Application/Control No. 10/039,289	Applicant(s)/Patent Under Reexamination WOLRICH ET AL.	
	Examiner Srirama Channavajjala	Art Unit 2166	Page 1 of 3

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,813,249	11-2004	Lauffenburger et al.	370/253
	B	US-6,393,531	05-2002	Novak et al.	711/154
	C	US-6,757,791	06-2004	O'Grady et al.	711/154
	D	US-5,892,979	04-1999	Shiraki et al.	710/52
	E	US-5,873,089	02-1999	Regache, Pascal	707/100
	F	US-5,671,446	09-1997	Rakity et al.	710/54
	G	US-6,779,084	08-2004	Wolrich et al.	711/118
	H	US-5,893,162	04-1999	Lau et al.	711/153
	I	US-2003/0115426	06-2003	Rosenbluth et al.	711/154
	J	US-2003/0115347	06-2003	Wolrich et al.	709/230
	K	US-2003/0110166	06-2003	Wolrich et al.	707/3
	L	US-6,438,651	08-2002	Slane, Albert Alfonse	711/118
	M	US-6,810,426	10-2004	Mysore et al.	709/234

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	EP 0760501 A1	03-1997	European	Regache,Pascal	5/06
	O	WO 98/25210	06-1998	PCT	Follett,David R et al.	15/16
	P	WO 03/017541 A1	02-2003	PCT	Haywood,Chris	1/16
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	G.Kornaros et al. "A fully-programmable memory management system optimizing queue handling at multi gigabit rates", DAC 2003, ACM pp 54-59
	V	Sivarama P Dandamudi, "multiprocessors", IEEE computer, March 1997, pp 82-89
	W	Maged M Michael ; "scalable lock-free dynamic memory allocation", PLDI,'04, ACM, June 2004, pp 1-12
	X	Sailesh Kumar et al. "A scalable, cache-based queue management subsystem for network processors", no date, pp 1-7

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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	A	US-			
	B	US-			
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	D	US-			
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	F	US-			
	G	US-			
	H	US-			
	I	US-			
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	K	US-			
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	M	US-			

FOREIGN PATENT DOCUMENTS

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	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Alper Buyuktosunoglu et al. "tradeoffs in power-efficient issue queue design", ISLPED'02, ACM, Aug, 2002, 6 pages
	V	Mathew Adiletta et al. "the next generation of Intel IXP network processors", 2002, pp 6-18
	W	Henk Jonkers, "queueing models of shared-memory parallel applications", computer and telecommunication systems performance engineering, 13 pages
	X	Lorraine McLuckie et al. "using the RapidIO messaging Unit on power QUICC III, freescale semiconductor, Inc, 2004 Rev 1, pp 1-19

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Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
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	M	US-			

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	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Tatyana Y Lyman et al. "data streams organization in query executor for parallel DBMS", no date, 4 pages
	V	Danny Hendler et al. "work dealing", SPAA '02, ACM, Aug 2002, pp 164
	W	Michael L Scott, "non-blocking timeout in scalable queue-based spin locks",PODC,2002, ACM, July 2002, pp 31-40
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.